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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s):	Linus Torvalds, Robert Bedichek and Stephen Johnson

Group Art Unit: 2183

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Application No.: 09/417,980

Title: A METHOD FOR TRANSLATING INSTRUCTIONS IN A SPECULATIVE MICROPROCESSOR FEATURING COMMITTING STATE (AS AMENDED)

Form 1449

U.S. Patent Documents

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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
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	F						
	G						
	H						
	I						
	J						
	K						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	Translation No
	L							
	M							
	N							
	O							
	P							
	Q							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
RLE	R	Wen-Mei W. Hwa and Yale N. Patt, CHECKPOINT REPAIR FOR OUT-OF-ORDER EXECUTION MACHINES, 1987, Proceedings of the 14th Annual International Symposium on Computer Architecture
RLE	S	Alberto Ferreira De Souza and Peter Rounce, DYNAMICALLY SCHEDULING THE TRACE PRODUCED DURING PROGRAM EXECUTION INTO VLIW INSTRUCTIONS, 04/12/99, Proceedings of the 13th International Parallel Processing Symposium 10th Symposium on Parallel and Distributed Programming

Richard Ellis   c/14/2005

RCE	T	Alberto Ferreira De Souza, Home Page, retrieved 01/20/05, http://www.inf.ufes.br/alberto/papers/desouza_rounce99a.pdf)>
Examiner Richard Ellis		Date Considered 4/14/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.